Access Memory (RAM) including dynamic and/or static RAM, on-chip or off-chip cache memory, and/or the like. Non-volatile memory **42**, which may be embedded and/or removable, may include, for example, read-only memory, flash memory, magnetic storage devices, for example, hard disks, floppy disk drives, magnetic tape, optical disc drives and/or media, non-volatile random access memory (NVRAM), and/or the like. Like volatile memory **40**, non-volatile memory **42** may include a cache area for temporary storage of data. At least part of the volatile and/or non-volatile memory may be embedded in processor **20**. The memories may store one or more software programs, instructions, pieces of information, data, and/or the like which may be used by the apparatus for performing functions of the user equipment/mobile terminal. The memories may comprise an identifier, such as for example an international mobile equipment identification (IMEI) code, capable of uniquely identifying apparatus **10**. The functions may include one or more of the operations disclosed herein with respect to the user equipment, such as for example the functions disclosed at process **400**. The memories may comprise an identifier, such as for example, an international mobile equipment identification (IMEI) code, capable of uniquely identifying apparatus **10**. In the example embodiment, the processor **20** may be configured using computer code stored at memory **40** and/or **42** to provide one or more operations described with respect to process **400**, FIG. **2**, FIG. **3**, and/or the like as disclosed herein.

[0050] Some of the embodiments disclosed herein may be implemented in software, hardware, application logic, or a combination of software, hardware, and application logic. The software, application logic, and/or hardware may reside on memory **40**, the control apparatus **20**, or electronic components, for example. In some example embodiment, the application logic, software or an instruction set is maintained on any one of various conventional computer-readable media. In the context of this document, a "computer-readable medium" may be any non-transitory media that can contain, store, communicate, propagate or transport the instructions for use by or in connection with an instruction execution system, apparatus, or device, such as for example a computer or data processor circuitry, with examples depicted at FIG. **5** and the like. A computer-readable medium may comprise a non-transitory computer-readable storage medium that may be any media that can contain or store the instructions for use by or in connection with an instruction execution system, apparatus, or device, such as for example a computer. Furthermore, some of the embodiments disclosed herein include computer programs configured to cause methods as disclosed herein (see, for example, process **400** and/or the like).

[0051] Without in any way limiting the scope, interpretation, or application of the claims appearing below, a technical effect of one or more of the example embodiments disclosed herein is a simple serial communication can be established between USB-device and accessory without the need to implement full power delivery (PD) communication. Moreover, without in any way limiting the scope, interpretation, or application of the claims appearing below, another technical effect of one or more of the example embodiments disclosed herein is reduction in the need for extra pins in the connector as unused CC-pins can be used for communications.

[0052] If desired, the different functions discussed herein may be performed in a different order and/or concurrently with each other. Furthermore, if desired, one or more of the

above-described functions may be optional or may be combined. Although various aspects of the invention are set out in the independent claims, other aspects of the invention comprise other combinations of features from the described embodiments and/or the dependent claims with the features of the independent claims, and not solely the combinations explicitly set out in the claims. It is also noted herein that while the above describes example embodiments, these descriptions should not be viewed in a limiting sense. Rather, there are several variations and modifications that may be made without departing from the scope of the present invention as defined in the appended claims. Other embodiments may be within the scope of the following claims. The term "based on" includes "based on at least."

What is claimed:

1. A method comprising:
   detecting, by a first device including a data interface, a current flow at a first communication control pin at the data interface; and
   assigning, by the first device based on the detection of the current flow at the first communication control pin, serial data communication circuitry to a second communication control pin at the data interface to carry serial data communications to another device.

2. The method of claim **1**, wherein the data interface comprises at least one of a universal serial bus connector and a universal serial bus receptacle.

3. The method of claim **1**, wherein the first communication control pin and the second communication control pin are coupled to at least pull-down resistor and a ground.

4. The method of claim **3**, wherein the first communication control pin is coupled to the other device including a pull-up resistor causing the current flow when coupled.

5. The method of claim **1**, wherein the first communication control pin is located at a first row of the data interface, and wherein the second communication control pin is located at a second row of the data interface.

6. The method of claim **1**, wherein the detecting further comprises identifying the first communication control pin as actively carrying communication control signaling, and wherein the assigning further comprises selecting the second communication control pin that is not actively carrying communication control signaling to carry the serial data.

7. The method of claim **1**, further comprising:
   detecting, by the first device, a current flow at the second communication control pin; and
   assigning, by the first device based on the detection of the current flow at the second communication control pin, the serial data communication circuitry to the first communication control pin to carry serial data communications to the other device.

8. An apparatus comprising:
   a data interface including a first communication control pin and a second communication control pin, the first communication control pin and a second communication control pin coupled to a pull-down resistor;
   detection circuitry to detect a current flow at the first communication control pin; and
   control circuitry to assign, based on the detection of the current flow at the first communication control pin, serial data communication circuitry to the second communication control pin to enable serial data communications between the apparatus and another device.